

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
BEFORE THE BOARD OF PATENT APPEALS AND INTERFERENCES

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First-Named Inventor: Martijn J. L. EMONS
Application No.: 09/829,793 Conf.: 1893
Date Filed: 04/10/2001

Atty Docket No.: NL 000215
Art Unit: 2186
Examiner: Kim, Hong Chong

Title: CACHE INTERFACE CIRCUIT FOR AUTOMATIC CONTROL OF CACHE BYPASS MODES AND
ASSOCIATED POWER SAVINGS

Commissioner for Patents
P.O. Box 1450
Arlington, VA 22313-1450

NOTICE OF APPEAL

Sir:


Applicant(s) hereby appeal(s) to the Board of Patent Appeals and Interferences from the
decision dated 06/08/2005 of the Examiner finally rejecting claims 6-8.

- ☒ Please charge the fee of \$500.00 to Deposit Account No. 14-1270.
☐ No additional fee is required, because the fee was paid in a prior appeal.

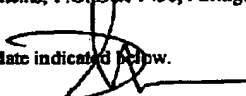
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Date: 9/8/05

Respectfully submitted,

By 
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(Date) <u>8-Sep-2005</u>	(Signature) <u></u>
	(Name) <u>Daniel L. Michalek</u>

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